



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#101A
22
2/2/04

In re Application of:
Sompong P. Olarig

Serial No.: 09/769,953

Filed: January 25, 2001

For: COMPUTER SYSTEM HAVING
CONFIGURABLE CORE LOGIC CHIPSET
FOR CONNECTION TO A FAULT-
TOLERANT ACCELERATED GRAPHICS
PORT BUS AND PERIPHERAL
COMPONENT INTERCONNECT BUS

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Group Art Unit: 2114

Examiner: Le, Dieu Minh T

Atty. Docket: 200304082-1
COMP:0195
(P98-1950)

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Technology Center 2100

Box Non-Fee Amendment
Commissioner for Patents
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37 C.F.R. 1.8

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3/19/04
Date

Robert A. Manware

Sir:

RESPONSE TO OFFICE ACTION MAILED DECEMBER 19, 2003

In response to the Official Action mailed on December 19, 2003 Applicant respectfully requests reconsideration of the above-identified application in view of the remarks and amendments set forth below.